

WEST[Help](#)[Logout](#)[Interrupt](#)[Main Menu](#)[Search Form](#)[Posting Counts](#)[Show S Numbers](#)[Edit S Numbers](#)[Preferences](#)**Search Results -**

Term	Documents
FIRST.DWPI,TDBD,EPAB,JPAB,USPT.	3954442
FIRSTS.DWPI,TDBD,EPAB,JPAB,USPT.	447
MEMORY.DWPI,TDBD,EPAB,JPAB,USPT.	1026402
MEMORIES.DWPI,TDBD,EPAB,JPAB,USPT.	118689
MEMORYS.DWPI,TDBD,EPAB,JPAB,USPT.	148
PATTERN\$1	0
PATTERN.DWPI,TDBD,EPAB,JPAB,USPT.	915861
PATTERNA.DWPI,TDBD,EPAB,JPAB,USPT.	20
PATTERNND.DWPI,TDBD,EPAB,JPAB,USPT.	9
PATTERNE.DWPI,TDBD,EPAB,JPAB,USPT.	16
((FIRST MEMORY) NEAR5 (PATTERN\$1 BIT\$1)) .USPT,JPAB,EPAB,DWPI,TDBD.	3

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Database:

US Patents Full-Text Database
 JPO Abstracts Database
 EPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Refine Search:

(first memory) near5 (pattern\$1 bit\$1)

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Today's Date: 2/23/2001

<u>DB Name</u>	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u>
USPT,JPAB,EPAB,DWPI,TDBD	(first memory) near5 (pattern\$1 bit\$1)	3	<u>L50</u>

USPT,JPAB,EPAB,DWPI,TDBD	l48 and (pattern\$1 near5 bit\$1)	1094	L49
USPT,JPAB,EPAB,DWPI,TDBD	(second memory) and l47	22857	L48
USPT,JPAB,EPAB,DWPI,TDBD	(first memory)	28774	L47
USPT,JPAB,EPAB,DWPI,TDBD	(network\$ near5 wakeup\$) and l36	0	L46
USPT,JPAB,EPAB,DWPI,TDBD	(network\$ near5 wakeup\$) and l36	0	L45
USPT,JPAB,EPAB,DWPI,TDBD	l43 and l36	3	L44
USPT,JPAB,EPAB,DWPI,TDBD	4103331.uref.	34	L43
USPT,JPAB,EPAB,DWPI,TDBD	l37 and (wakeup\$)	1	L42
USPT,JPAB,EPAB,DWPI,TDBD	l37 and (network\$ near5 wakeup\$)	0	L41
USPT,JPAB,EPAB,DWPI,TDBD	6049885.uref.	0	L40
USPT,JPAB,EPAB,DWPI,TDBD	(4730251 5089813 5196728 5305321 5404544 5465926 5475847 5485625 5495516 5517620 5530808 5609560 5734842 5835719)![pn]	37	L39
USPT,JPAB,EPAB,DWPI,TDBD	6049885.pn.	2	L38
USPT,JPAB,EPAB,DWPI,TDBD	l36 and (ethernet near5 interfac\$)	322	L37
USPT,JPAB,EPAB,DWPI,TDBD	(dual or multi\$) near5 (ram\$1 or memor\$2)	23116	L36
USPT,JPAB,EPAB,DWPI,TDBD	(ethernet and wakeup and interfac\$).ti.	0	L35
USPT,JPAB,EPAB,DWPI,TDBD	(ethernet and wakeup and card\$).ti.	0	L34
USPT,JPAB,EPAB,DWPI,TDBD	(ethernet and wakeup and chip\$).ti.	0	L33
USPT,JPAB,EPAB,DWPI,TDBD	(ethernet and wakeup and circuit\$).ti.	0	L32
USPT,JPAB,EPAB,DWPI,TDBD	(ethernet and wakeup and frame and pattern\$).ti.	0	L31
USPT,JPAB,EPAB,DWPI,TDBD	l25 and (wake\$up\$)	26	L30
USPT,JPAB,EPAB,DWPI,TDBD	l25 and (network\$ same wake\$up\$)	0	L29
USPT,JPAB,EPAB,DWPI,TDBD	l25 and ((pattern) near5 (enable\$ or disable\$))	0	L28
USPT,JPAB,EPAB,DWPI,TDBD	l25 and (ethernet near5 MII)	0	L27
USPT,JPAB,EPAB,DWPI,TDBD	l25 and (ethernet near5 mac)	0	L26
USPT,JPAB,EPAB,DWPI,TDBD	l23 and (count\$2 near5 register\$1)	26	L25
USPT,JPAB,EPAB,DWPI,TDBD	l23 and (index\$3 near5 count\$)	0	L24
USPT,JPAB,EPAB,DWPI,TDBD	l21 and (network\$ or lan or wan)	26	L23
USPT,JPAB,EPAB,DWPI,TDBD	l21 and (multi\$3 near5 pattern\$1)	0	L22
USPT,JPAB,EPAB,DWPI,TDBD	l20 and (l28 near5 byte\$)	26	L21
USPT,JPAB,EPAB,DWPI,TDBD	l18 and (four near5 byte\$1)	26	L20
USPT,JPAB,EPAB,DWPI,TDBD	l18 and (access\$3 near5 pattern\$1)	0	L19
USPT,JPAB,EPAB,DWPI,TDBD	l17 and (data near5 frame\$1)	26	L18
USPT,JPAB,EPAB,DWPI,TDBD	l16 and ((dual or multi) near5 (memory or ram\$1))	26	L17

USPT,JPAB,EPAB,DWPI,TDBD	114 and (mask\$ near5 bit\$1)	26	L16
USPT,JPAB,EPAB,DWPI,TDBD	114 and (wakeup near5 match\$)	0	L15
USPT,JPAB,EPAB,DWPI,TDBD	113 and (frame\$1 near5 address\$2)	26	L14
USPT,JPAB,EPAB,DWPI,TDBD	112 and (PCI near5 interfac\$)	26	L13
USPT,JPAB,EPAB,DWPI,TDBD	110 and (data near5 bus)	32	L12
USPT,JPAB,EPAB,DWPI,TDBD	110 and (network\$ near5 wakeup\$)	0	L11
USPT,JPAB,EPAB,DWPI,TDBD	18 and (ethernet)	33	L10
USPT,JPAB,EPAB,DWPI,TDBD	18 and (ethernet near chip\$1)	0	L9
USPT,JPAB,EPAB,DWPI,TDBD	17 and (network near5 interfac\$)	36	L8
USPT,JPAB,EPAB,DWPI,TDBD	16 and (address\$2 near5 match\$)	75	L7
USPT,JPAB,EPAB,DWPI,TDBD	15 and (ram\$1 or stor\$)	250	L6
USPT,JPAB,EPAB,DWPI,TDBD	(pattern\$ near5 address\$1) and l4	353	L5
USPT,JPAB,EPAB,DWPI,TDBD	(mask\$ near5 address\$1)	3494	L4
USPT,JPAB,EPAB,DWPI,TDBD	((data match) near5 (data accumulat\$)).clm.	0	L3
USPT,JPAB,EPAB,DWPI,TDBD	((data match) near5 (data accumulat\$)) same (state machine)	0	L2
USPT,JPAB,EPAB,DWPI,TDBD	(data match state machine)	0	L1

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Search Results - Record(s) 1 through 3 of 3 returned.☐ 1. Document ID: US 5778000 A

L50: Entry 1 of 3

File: USPT

Jul 7, 1998

US-PAT-NO: 5778000

DOCUMENT-IDENTIFIER: US 5778000 A

TITLE: Frame synchronization method

DATE-ISSUED: July 7, 1998

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Dosiere; Philippe Richard	Brussels	N/A	N/A	BEX
Mennekens; Jan	Brasschaat	N/A	N/A	BEX
Sonck; Geert Alfons Domien	Herdersem	N/A	N/A	BEX
Marguinaud; Andre	Palaiseau	N/A	N/A	FRX

US-CL-CURRENT: 370/512; 370/509, 370/511, 714/704, 714/707

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWC	Draw Desc	Image
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☐ 2. Document ID: US 5535307 A

L50: Entry 2 of 3

File: USPT

Jul 9, 1996

US-PAT-NO: 5535307

DOCUMENT-IDENTIFIER: US 5535307 A

TITLE: Printing of variable dot sizes dependent upon image density for improved graphics

DATE-ISSUED: July 9, 1996

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Glass; Stephen K.	San Diego	CA	N/A	N/A

US-CL-CURRENT: 358/1.8; 347/5, 358/1.15, 358/1.9

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWC	Draw Desc	Image
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- ☐ 3. Document ID: DE 69515232 E, EP 697783 A1, US 5535307 A, EP 697783 B1
 L50: Entry 3 of 3 File: DWPI Apr 6, 2000

DERWENT-ACC-NO: 1996-107541
 DERWENT-WEEK: 200024
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TITLE: High resolution half tone printing e.g. for graphics or text pattern - printing variable number of high resolution dots for each dot in low resolution graphics pattern

INVENTOR: GLASS, S K

PRIORITY-DATA: 1994US-0291230 (August 16, 1994)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
DE 69515232 E	April 6, 2000	N/A	000	H04N001/40
EP 697783 A1	February 21, 1996	E	008	H04N001/40
US 5535307 A	July 9, 1996	N/A	007	G06K015/00
EP 697783 B1	March 1, 2000	E	000	H04N001/40

INT-CL (IPC): G06K 15/00; H04N 1/40

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Draw Desc	Clip Img	Image
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Documents, starting with Document:

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